

/ Descriptions

TO-92 PNP Silicon PNP transistor in a TO-92 Plastic Package.

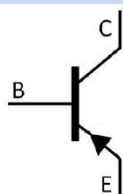
/ Features

Complementary pair with 2SC1815, excellent h_{FE} linearity, low noise, complementary pair with 2SC1815.
High voltage and high current, excellent h_{FE} linearity, low noise, complementary pair with 2SC1815.

/ Applications

Audio frequency general purpose, driver stage amplifier applications.

/ Equivalent Circuit



/ Pinning



PIN1 Base PIN 2 Collector PIN 3 Emitter

/ h_{FE} Classifications & Marking

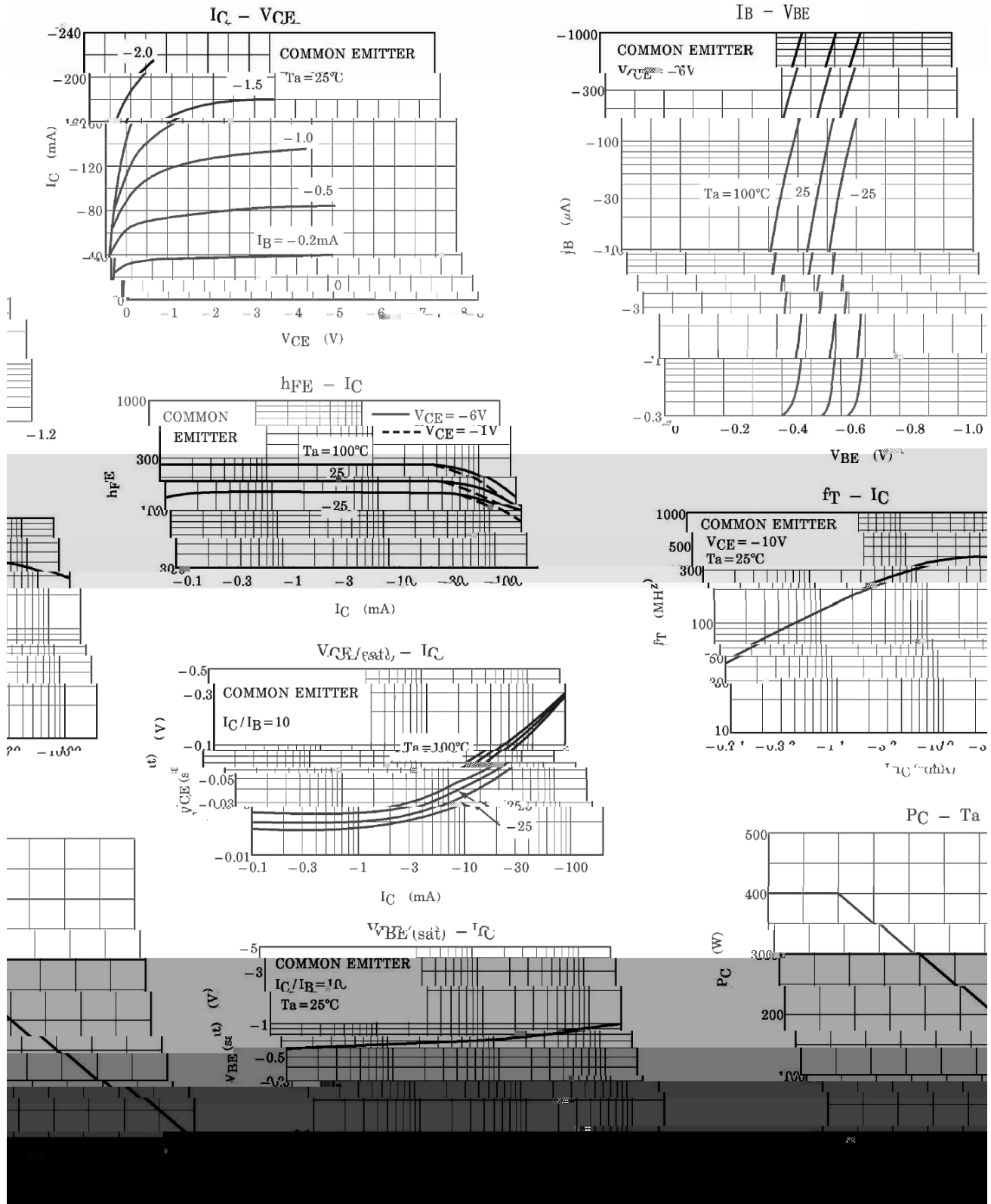
h_{FE} Classifications Symbol	O	Y	GR
h_{FE} Range	70~140	120~240	200~400

Parameter	Symbol	Rating	Unit
Collector to Base Voltage	V_{CBO}	-50	V
Collector to Emitter Voltage	V_{CEO}	-50	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-150	mA
Base Current – Continuous	I_B	-50	mA
Collector Power Dissipation	P_C	400	mW
Junction Temperature	T_J	150	
Storage Temperature Range	T_{stg}	-55~150	

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector Cut-Off Current	I_{CBO}	$V_{CB}=-50V$ $I_E=0$			-0.1	μA
Collector Cut-Off Current	I_{CEO}	$V_{EB}=-5.0V$ $I_C=0$			-0.1	μA
DC Current Gain	$h_{FE(1)}$	$V_{CE}=-6.0V$ $I_C=-2.0mA$	70		400	
	$h_{FE(2)}$	$V_{CE}=-6.0V$ $I_C=-150mA$	25			

Collector

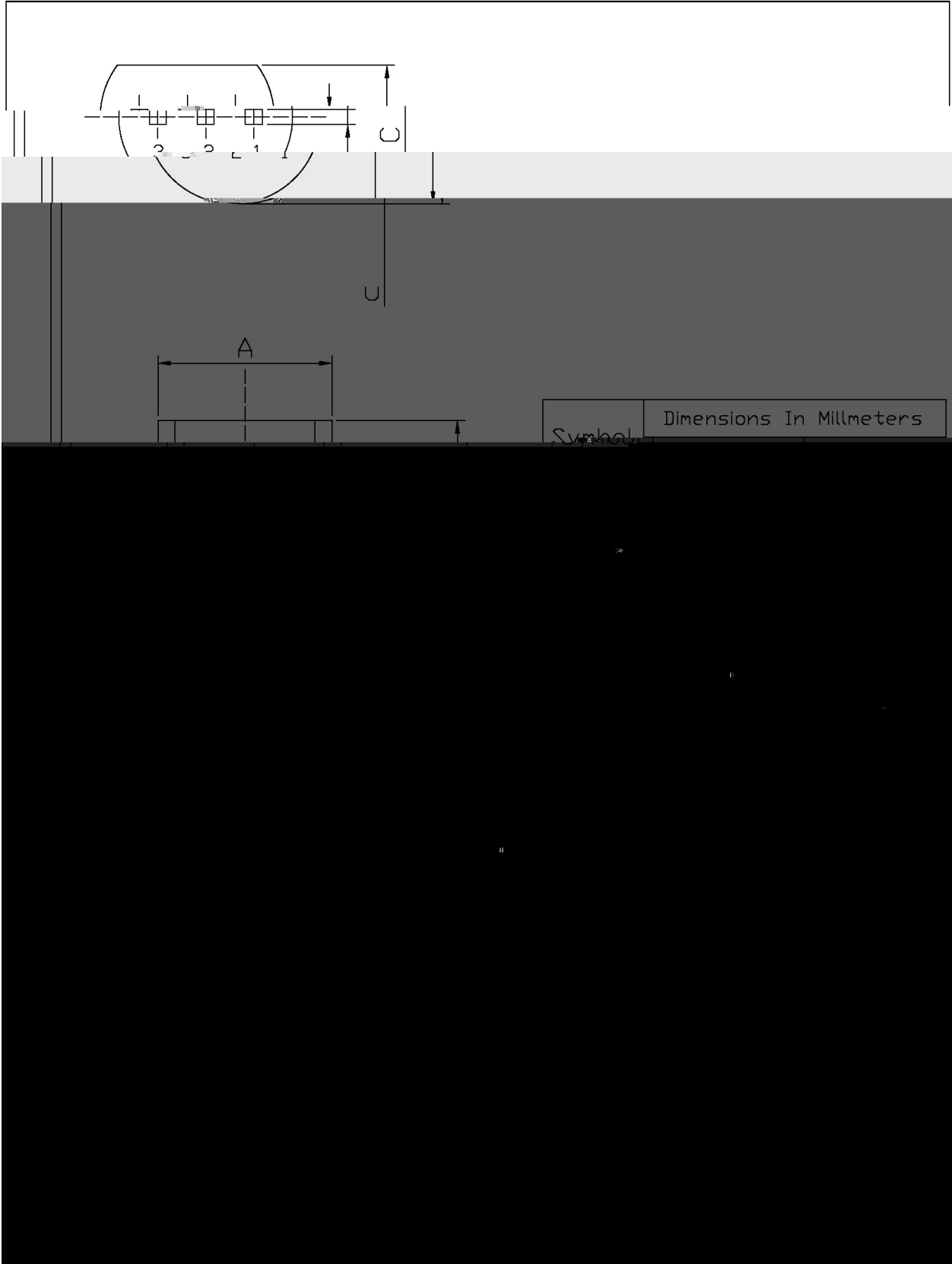
/ Electrical Characteristic Curve



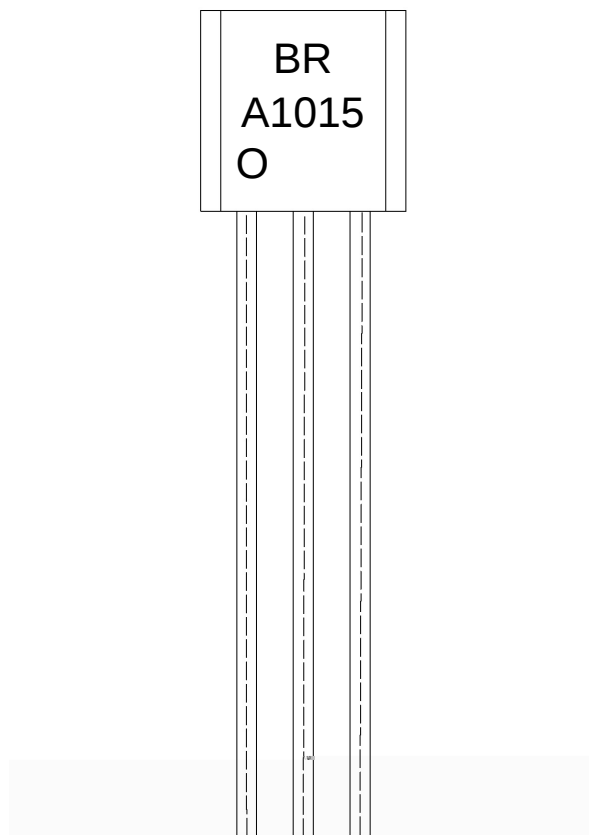
/ Package Dimensions

TO-92

Unit: mm



/ Marking Instructions



BR:

A1015

O: h_{FE}

Note:

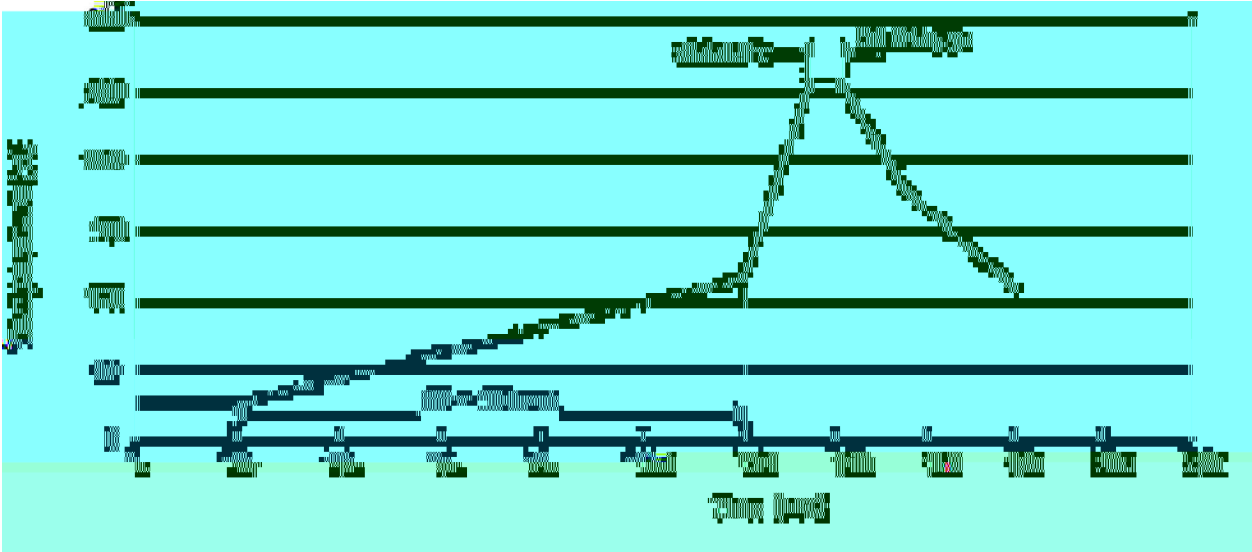
BR: Company Code.

A1015: Product Type.

O: h_{FE} Classifications Symbol

****: Lot No. Code, code change with Lot No.

() / Temperature Profile for Dip Soldering(Pb-Free)



- Note:

1

25 150

60 90sec;

1.Preheating:25~150 , Time:60~90sec.

2

255±5

5±0.5sec;

2.Peak Temp.:255±5 , Duration:5±0.5sec.

3

2 10 /sec.

3. Cooling Speed: 2~10 /sec.

/ Resistance to Soldering Heat Test Conditions

270±5

10±1 sec.

Temp:270±5

Time:10±1 sec

/ Packaging SPEC.

/ BULK

Package Type	Units				Dimension		(unit mm3)