

/ Descriptions			
BRCM24C08SC	SOP-8	8 Kbit	I ² C

/ Features			
1.7V	2.5~5.5V	1Mhz	1.7~2.5V
400Khz			
CMOS	400uA	1.6mA	
16			
	128		
	5ms		
	100	100	ESD HBM 6KV
+/-200mA;			

/ Applications

/ Pinning



/ Pinning

Pin	Name	Type	Description
1	NC	-	
2	NC	-	
3	E2	Input	
4	GND	Ground	
5	SDA	I/O	/
6	SCL	Input	
7	WCB	Input	
8	VCC	Power	

/ Marking

/ Absolute Maximum Ratings(Ta=25)

			mA
			V

/ Reliability Characteristic

		Ñ				

		○				
		○				

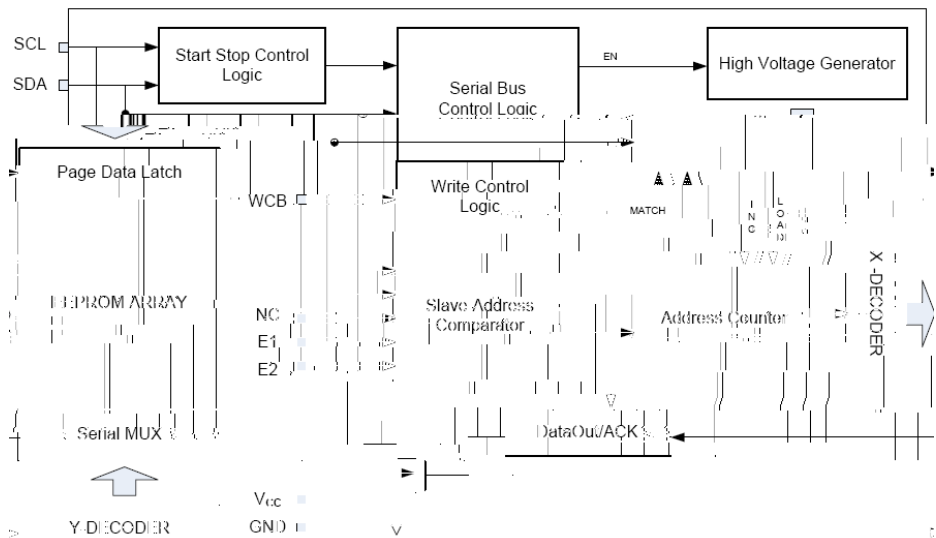
		≤			≤ ≤			

/ AC Electrical Characteristics(Unless otherwise specified, Vcc = 1.7V to 5.5V,

T_A = 40

		≤			≤ ≤			

≤



/ Functional Description

H / Data Input

SDA SDA SCL (1) SCL
(1)

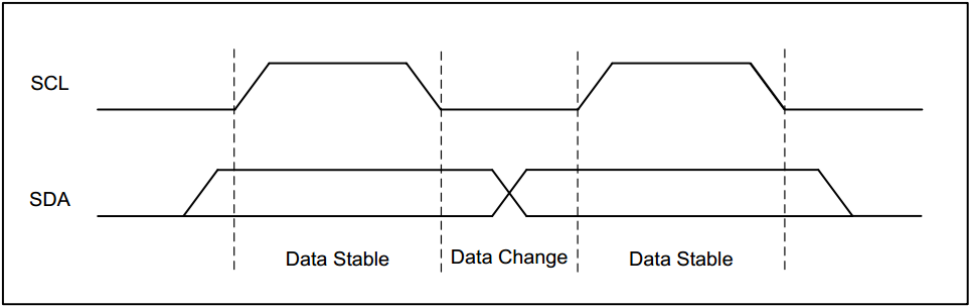


Figure 1 Data Validity

H / Start Condition

/ Functional Description

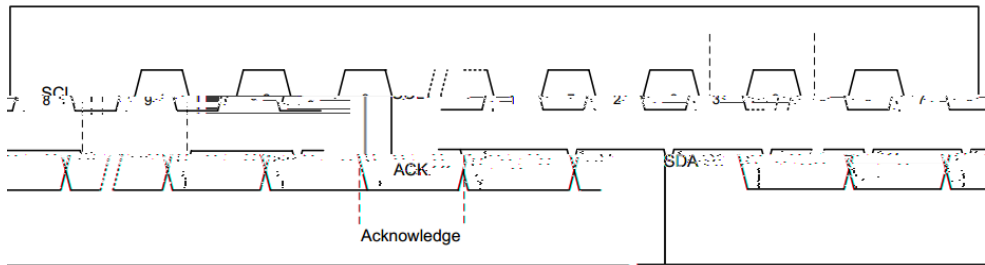


Figure 3 Output Acknowledge

H / Standby Mode

BRCM24C08SC : (a) , (b)
, (c)

H / Soft Reset

, (c) (a) , (b)
4

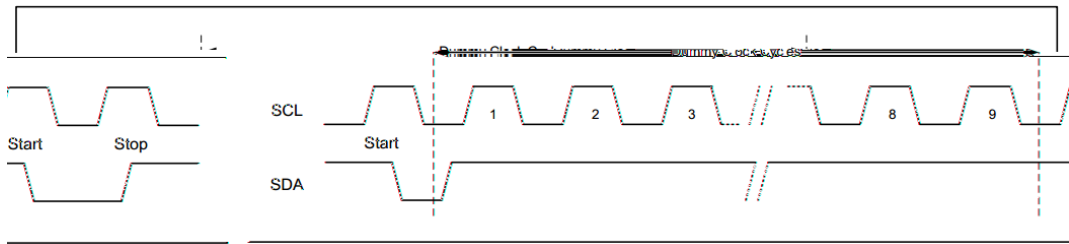


Figure 4 Soft Reset

H / Device Addressing

BRCM24C08SC 8 1
1 0

/ Functional Description

E228 / E20

10

H / Data Security

BRCM24C08SC WCB Vcc

H / Byte Write

8

BRCM24C08SC “ 0 ” 8 8

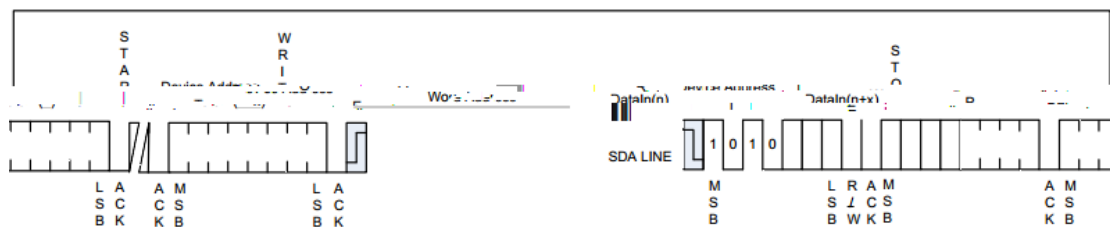
BRCM24C08SC “ 0 ”

BRCM24C08SC BRCM24C08SC

(5)

The diagram shows the timing of the 68000 bus signals. The clock signal is a square wave. The Device Address signal is shown as a sequence of 16-bit words. The Word Address signal is shown as a sequence of 16-bit words. The Data In signal is shown as a sequence of 16-bit words. The Data Out signal is shown as a sequence of 16-bit words. The data bus signals are shown in a sequence of 16-bit words.

Signal	Value
L	1
S	0
B	0
M	1
S	0
B	0
L	1
R	0
A	0
M	0
S	1
/C	0
S	0
B	0
W	1
K	0
B	0
A	1
M	0
C	0
S	0
K	1
B	0



/ Functional Description

H / Acknowledge Polling

BRCM24C08SC
/ BRCM24C08SC
“ 0 ”

H / Write Identification Page

16
(a) 1011b
(b) A5 / A4 0 1 A7 / A6 “ 00 ”
(c) A3 / A0
NoACK

H / Lock Identification Page

ID Lock ID
(a) 1011b;
(b) A6 1 ;
(c) xxxx xx1x x 0 1

/ Functional Description

H / Read Operations

/ " 1 "

;

H

/ Functional Description

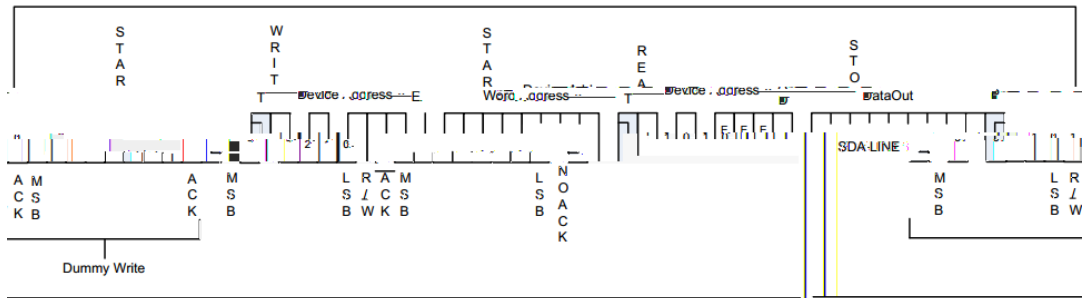


Figure 8 Random Address Read

H / Sequential Read

BRCM24C08SC

“ 0 ”

9

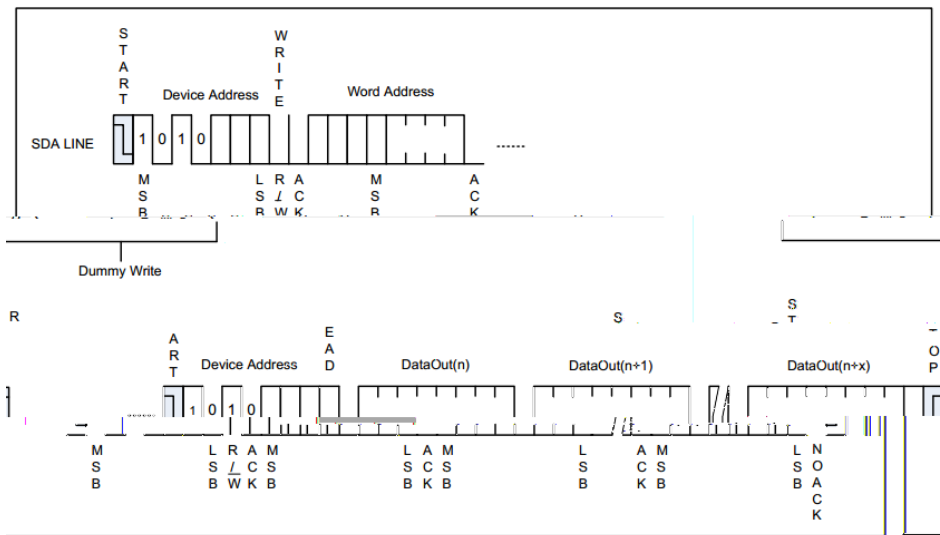
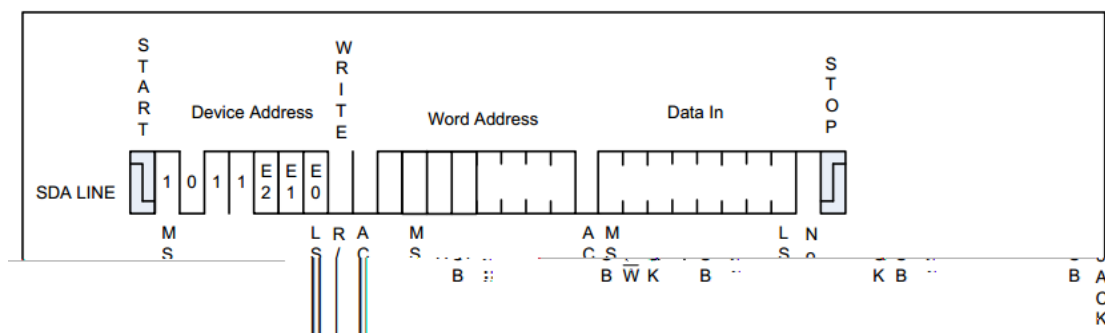


Figure 9

H / Read Identification Page

H / Read the Lock Status

$$\frac{[ACK] + [NoACK]}{10}$$


16

1

128

EEPROM

EEPROM

1

/ Functional Description

```

sequenceDiagram
    participant S
    participant R
    Note over S: 128 16
    S->>R: A7 A6 " 10 " 2
    Note over R: 128
    R->>S: ACK
    Note over R: 7
  
```

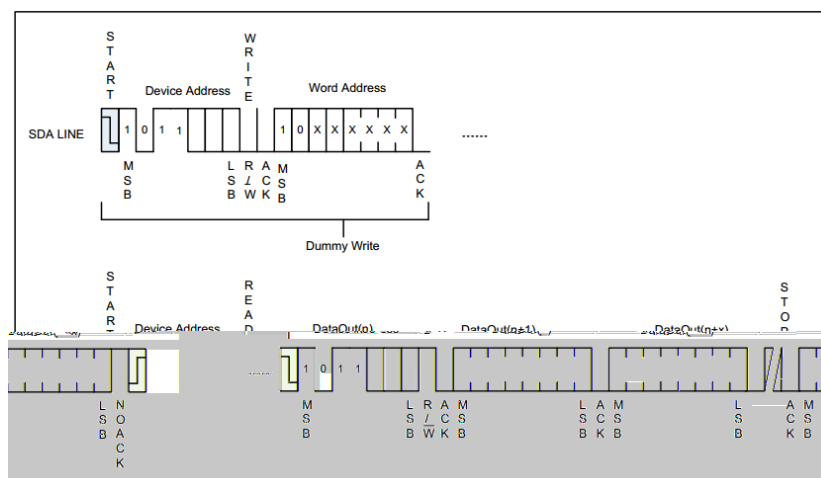
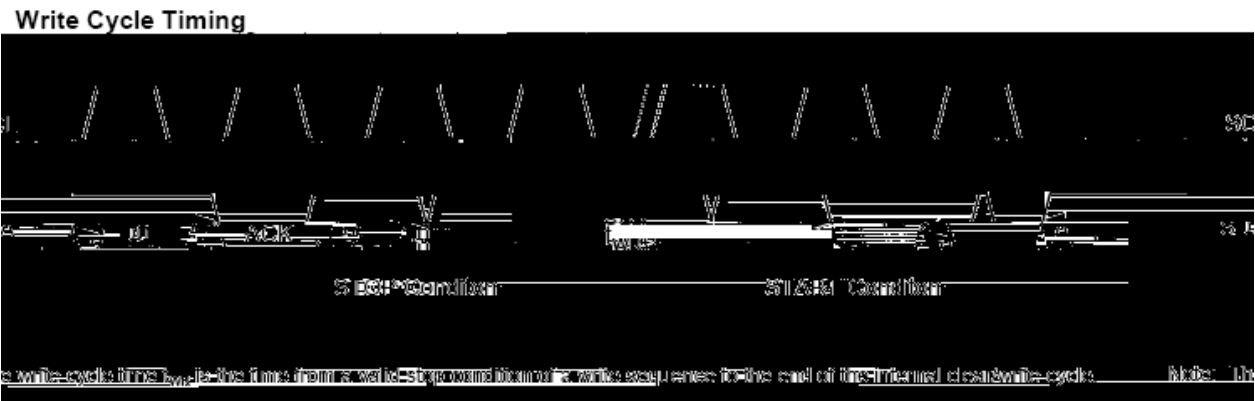
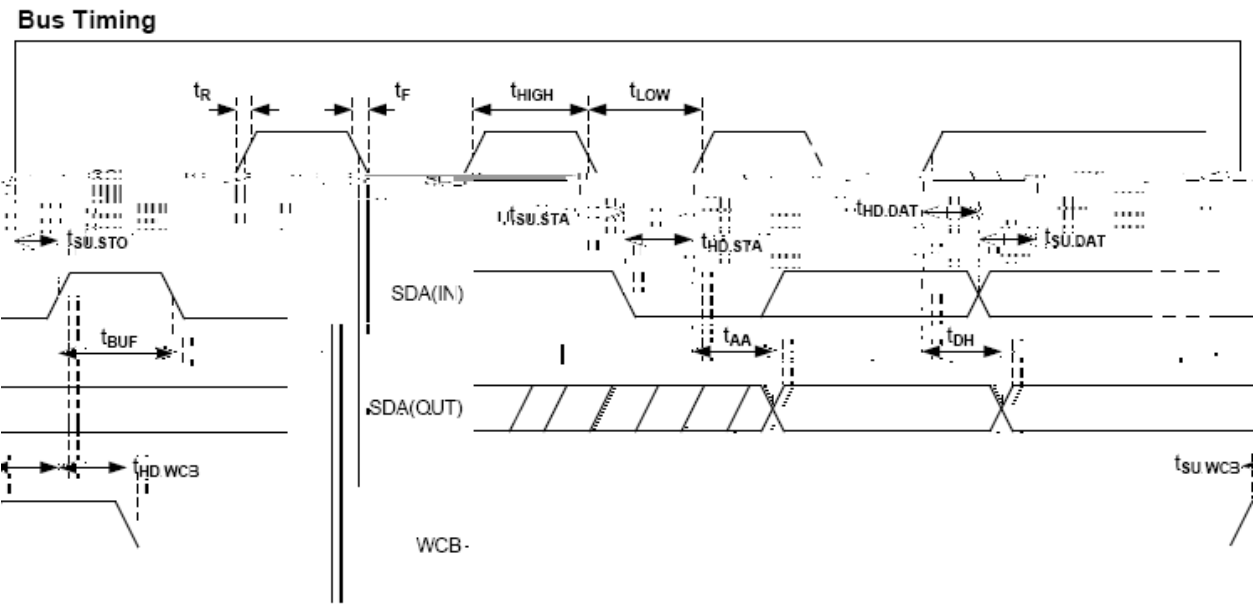
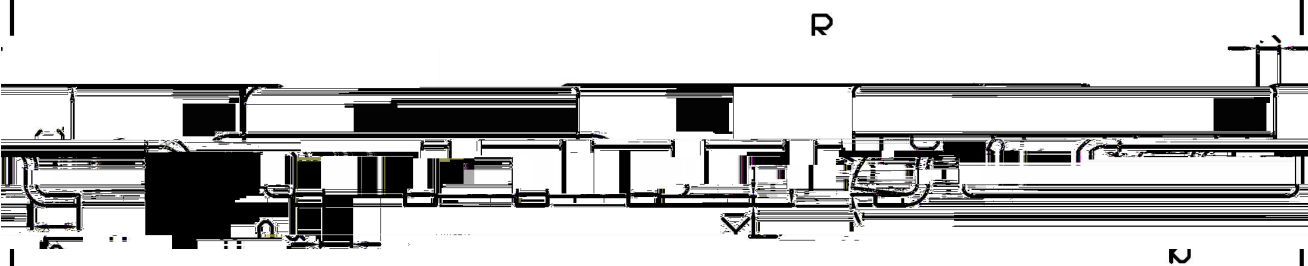
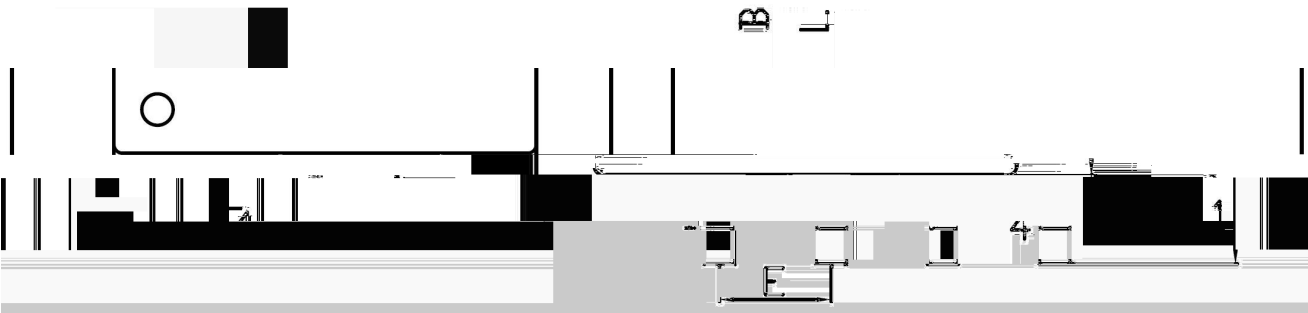
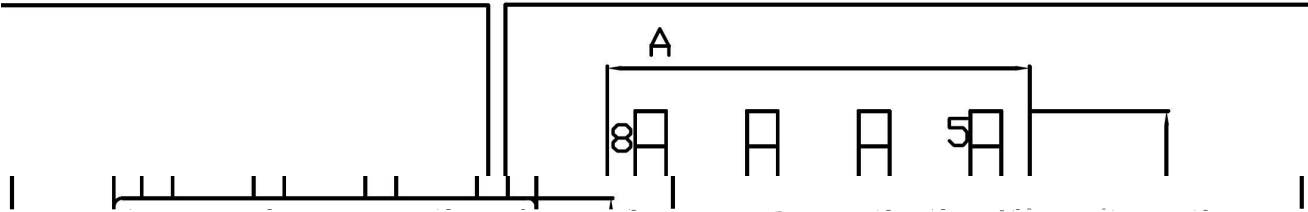


Figure 11 Sequential Read

/ Time Sequence Diagram

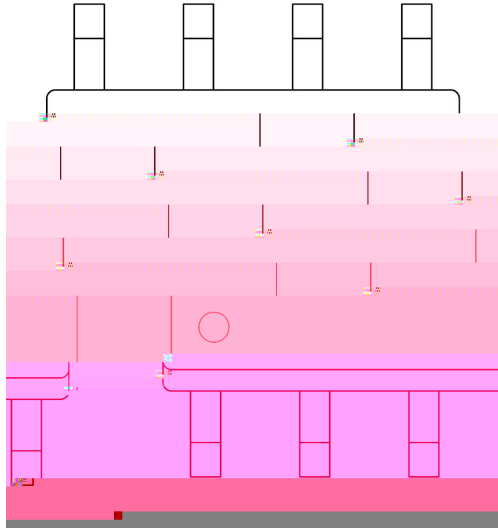


/ Package Dimensions



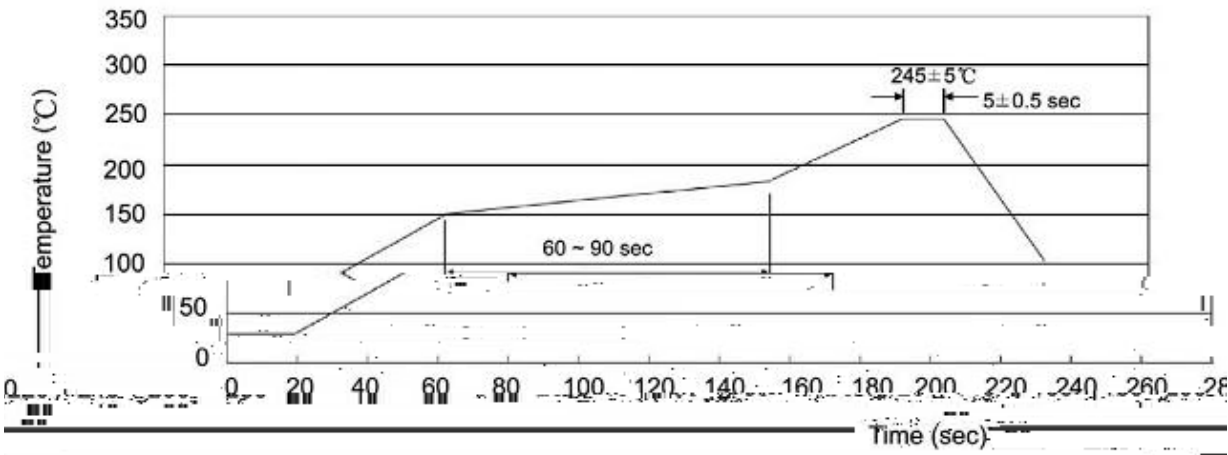
Dimensions In Millimeters				Dimensions In Millimeters			
Symbol	Min	Max	Typ	Symbol	Min	Max	Typ
A	1.27	1.27	1.27	A	1.27	1.27	1.27
B	1.27	1.27	1.27	B	1.27	1.27	1.27
C	1.27	1.27	1.27	C	1.27	1.27	1.27
D	1.27	1.27	1.27	D	1.27	1.27	1.27
E	1.27	1.27	1.27	E	1.27	1.27	1.27
F	1.27	1.27	1.27	F	1.27	1.27	1.27
G	1.27	1.27	1.27	G	1.27	1.27	1.27
H	1.27	1.27	1.27	H	1.27	1.27	1.27
I	1.27	1.27	1.27	I	1.27	1.27	1.27
J	1.27	1.27	1.27	J	1.27	1.27	1.27
K	1.27	1.27	1.27	K	1.27	1.27	1.27
L	1.27	1.27	1.27	L	1.27	1.27	1.27
M	1.27	1.27	1.27	M	1.27	1.27	1.27
N	1.27	1.27	1.27	N	1.27	1.27	1.27
O	1.27	1.27	1.27	O	1.27	1.27	1.27
P	1.27	1.27	1.27	P	1.27	1.27	1.27
Q	1.27	1.27	1.27	Q	1.27	1.27	1.27
R	1.27	1.27	1.27	R	1.27	1.27	1.27
S	1.27	1.27	1.27	S	1.27	1.27	1.27
T	1.27	1.27	1.27	T	1.27	1.27	1.27
U	1.27	1.27	1.27	U	1.27	1.27	1.27
V	1.27	1.27	1.27	V	1.27	1.27	1.27
W	1.27	1.27	1.27	W	1.27	1.27	1.27
X	1.27	1.27	1.27	X	1.27	1.27	1.27
Y	1.27	1.27	1.27	Y	1.27	1.27	1.27
Z	1.27	1.27	1.27	Z	1.27	1.27	1.27

/ Marking Instructions



24C08

() /



± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ± ± ±

/ Packaging SPEC.

封装形式	包装数量					包装尺寸		
	只 卷盘	卷盘 盒	只 盒	盒 箱	只 箱		盒	箱

/ Notices