

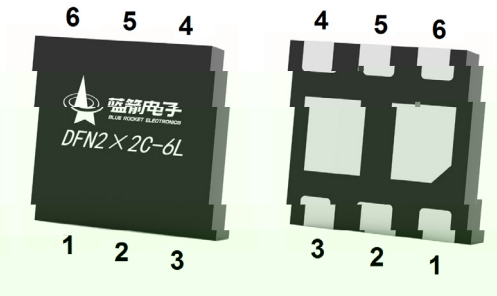
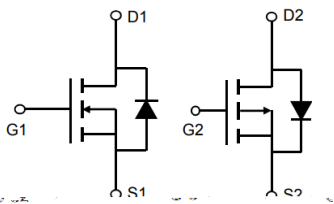
/ Descriptions

DFN2×2C-6L

x

/ Features

T9403 T Ap



出脚	定义
PIN 1	S1
PIN 2	G1
PIN 3	D2
PIN 4	S2
PIN 5	G2
PIN 6	D1

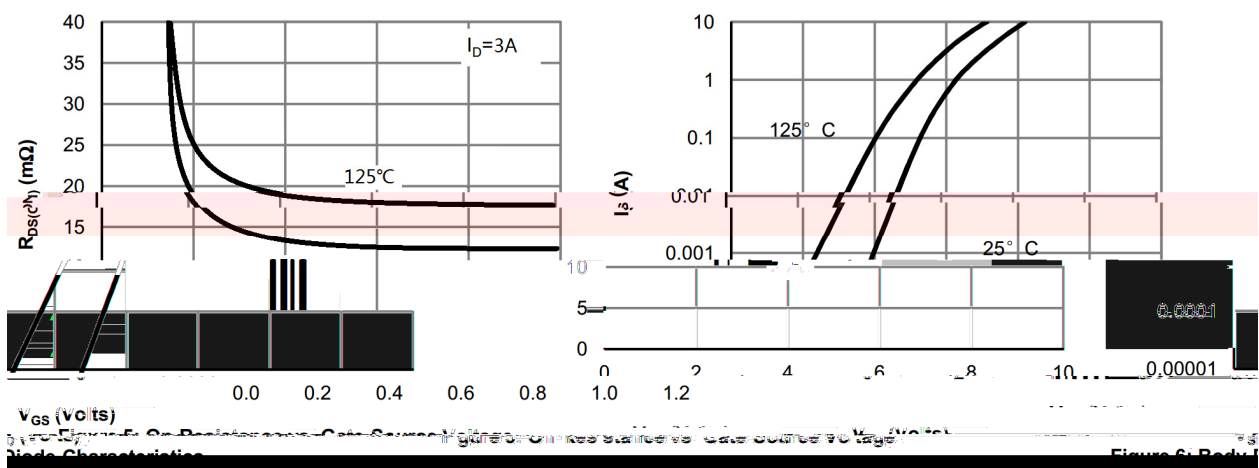
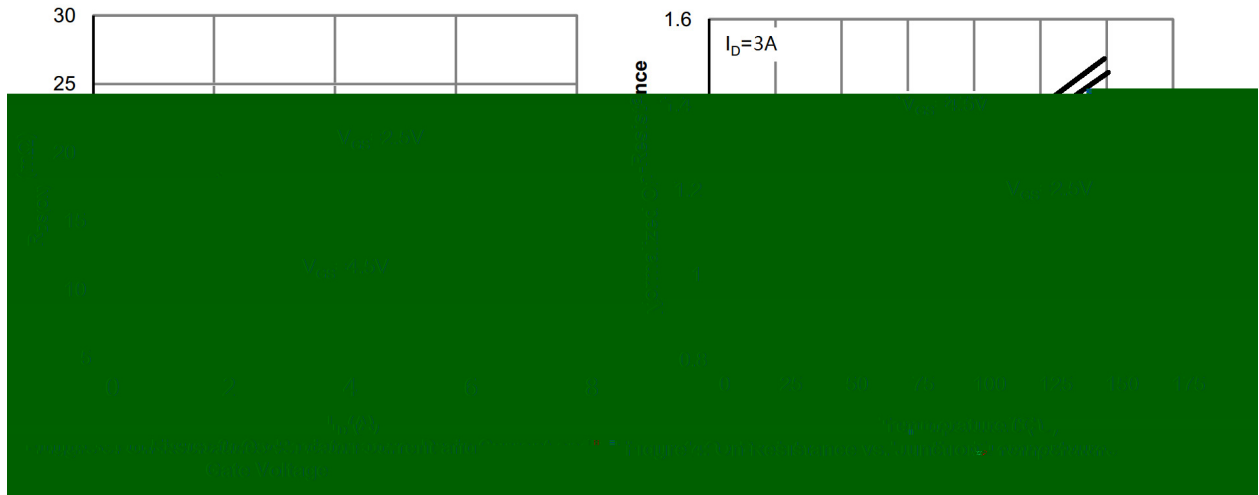
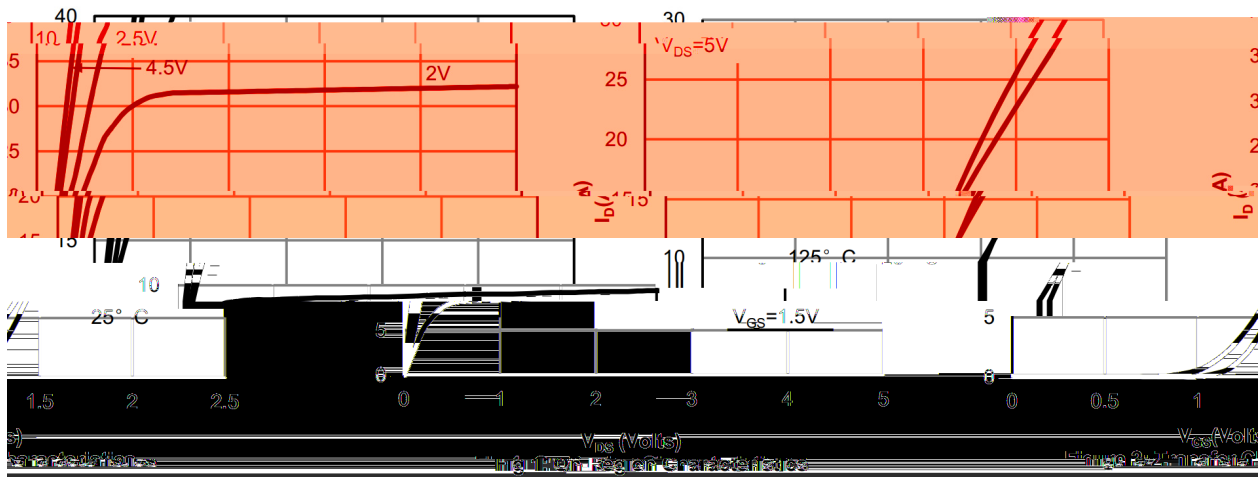
/ Absolute Maximum Ratings(T_a=25)

	$\theta \leq$			
	θ			

N- /N-CHANNEL Electrical Characteristics(Ta=25)

						Ω

N- / N-CHANNEL Electrical Characteristic Curve



N-

/ N-CHANNEL Electrical Characteristic Curve

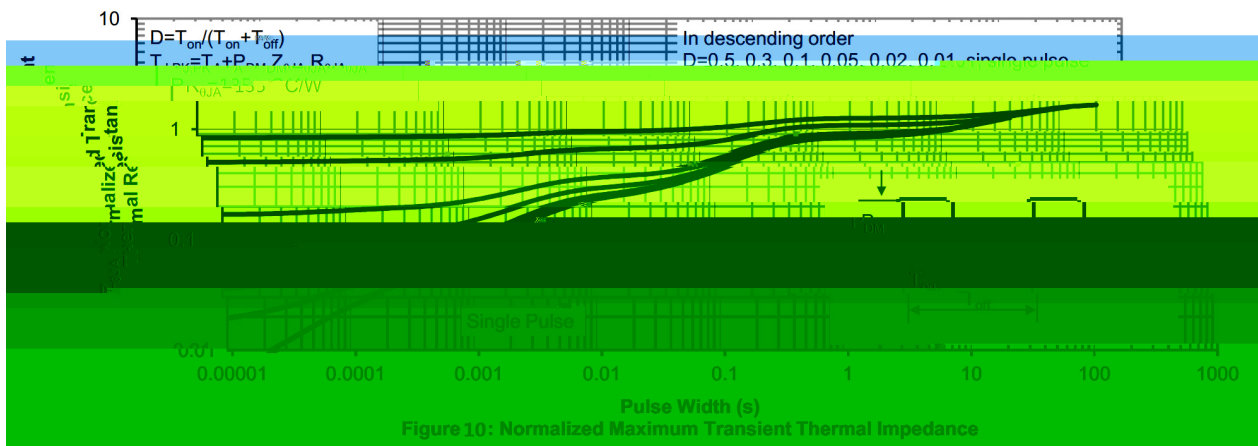
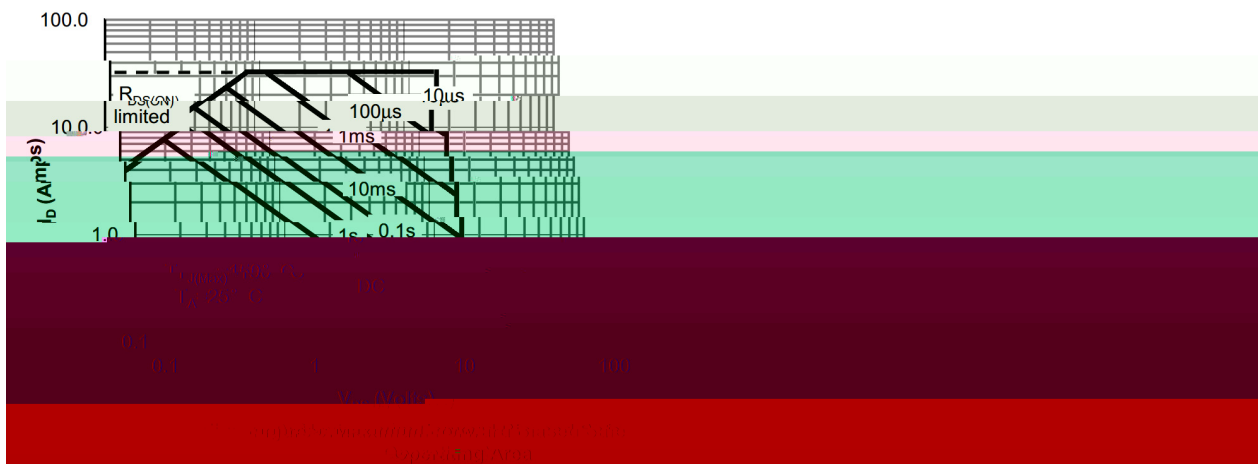
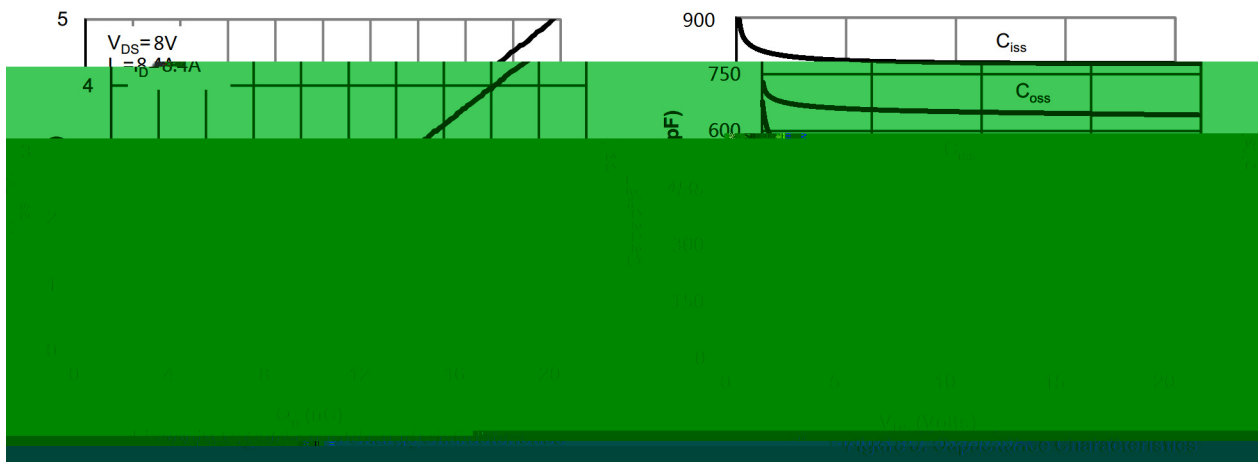
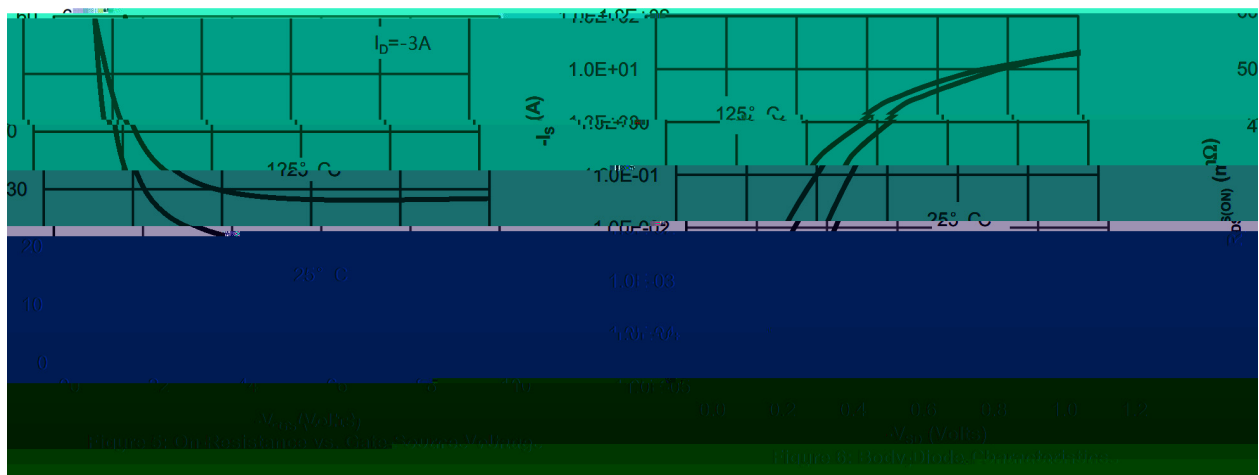
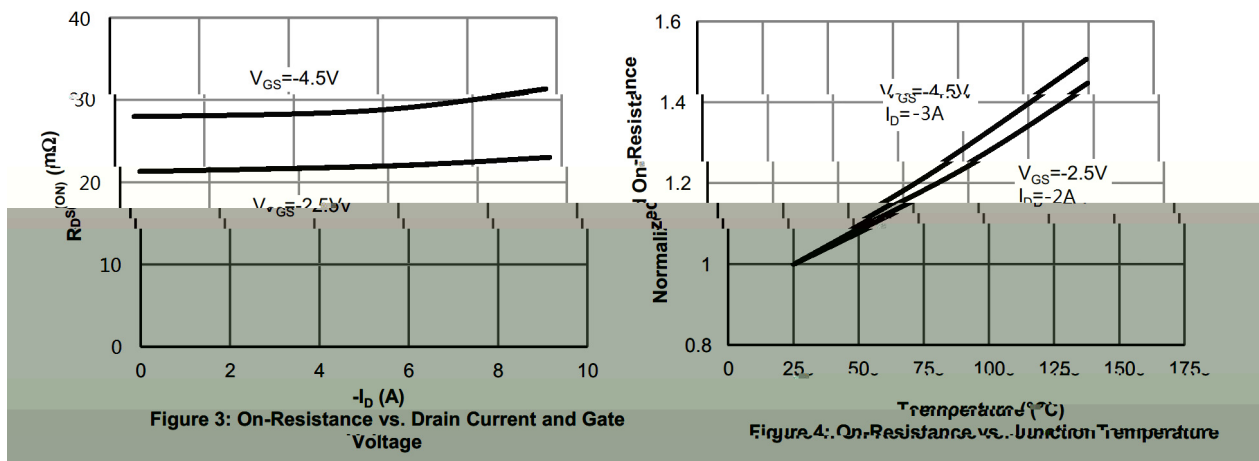
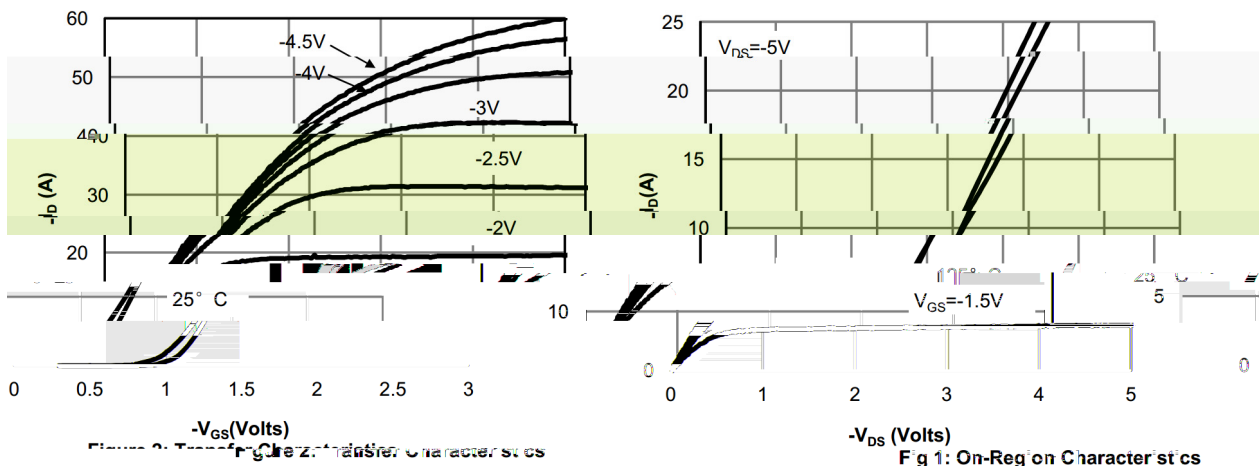


Figure 10: Normalized Maximum Transient Thermal Impedance

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P- / P-CHANNEL Electrical Characteristic Curve



P- / P-CHANNEL Electrical Characteristic Curve

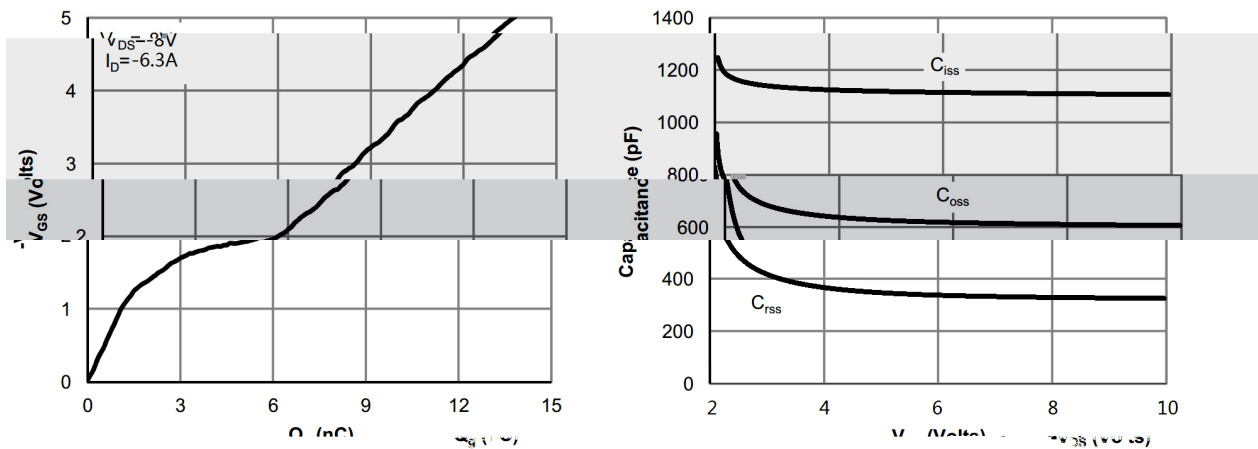


Figure 7: Gate Charge Characteristics

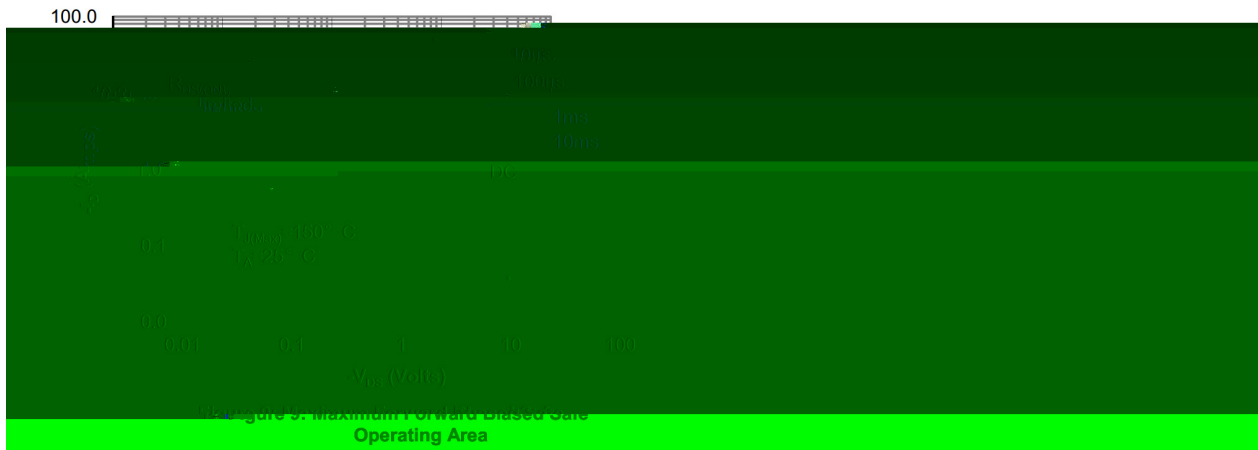


Figure 8: Maximum Forward Biased Safe Operating Area

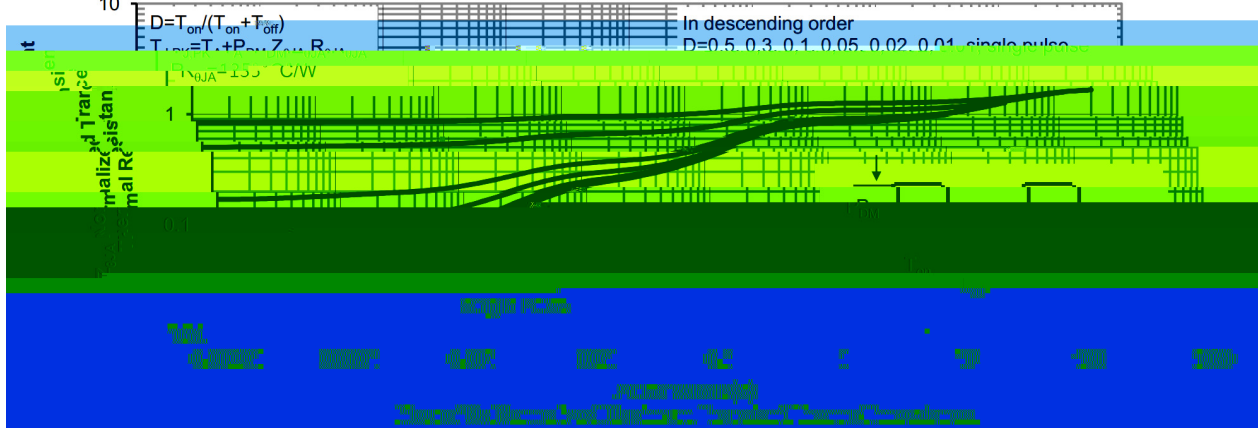
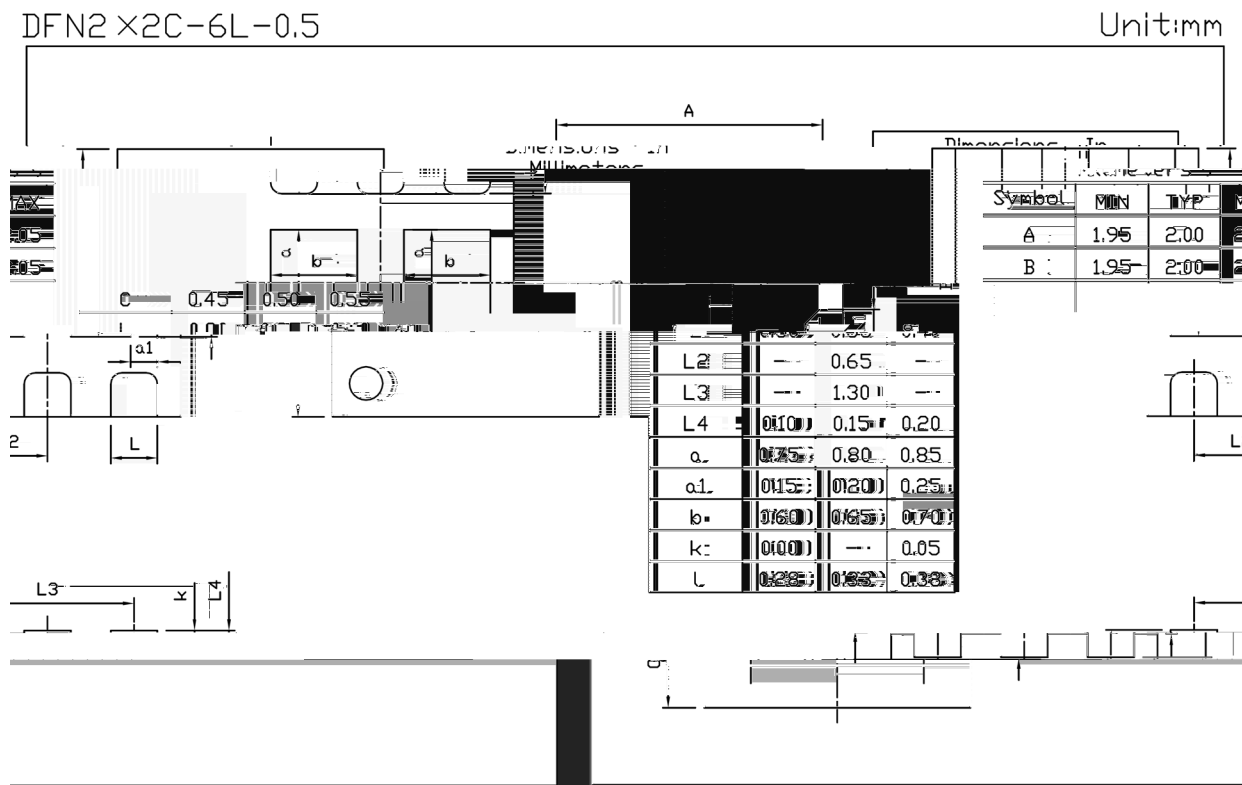
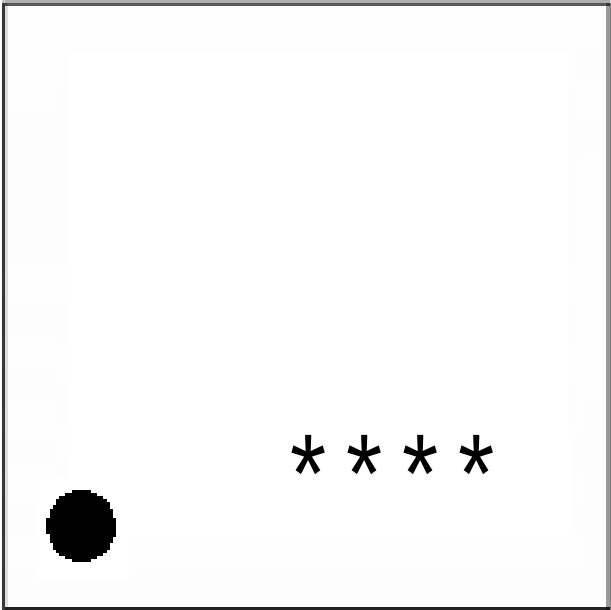


Figure 9: Thermal Resistance

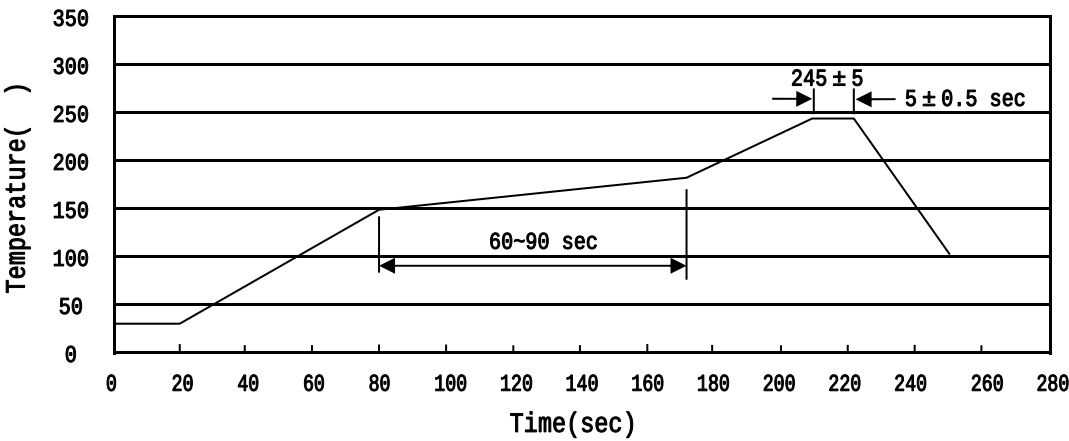
/ Package Dimensions



/ Marking Instructions



() / Temperature Profile for IR Reflow Soldering(Pb-Free)



± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ±

/ Packaging SPEC.

	Units/Reel /	Reels/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Reel	Inner Box	Outer Box

/ Notices